

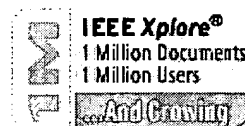
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L8	0	thermometer binary gate adders	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/11/18 12:49
L14	0	thermometer binary AND gates adders	USPAT	SAME	OFF	2004/11/18 13:02
L12	0	thermometer binary AND gates adder	USPAT	WITH	OFF	2004/11/18 13:01
L13	0	thermometer binary AND gates adders	USPAT	WITH	OFF	2004/11/18 13:02
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L21	1	thermometer binary multiplexor	USPAT	SAME	OFF	2004/11/18 14:47
L9	2	thermometer binary gates adder	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/11/18 12:54
L4	2	thermometer binary SAME adder SAME gate	US-PGPUB; USOCR; EPO; JPO; DERWENT	WITH	OFF	2004/11/18 12:33
L5	2	thermometer binary SAME adder SAME gate	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2004/11/18 12:34
L15	7	thermometer binary AND gates adder	USPAT	SAME	OFF	2004/11/18 13:25
L23	8	thermometer binary adding	USPAT	SAME	OFF	2004/11/18 14:57
L3	8	thermometer code binary SAME adder	US-PGPUB; USOCR; EPO; JPO; DERWENT	WITH	OFF	2004/11/18 12:32
L2	9	thermometer code binary SAME adder	USPAT	WITH	OFF	2004/11/18 12:24
L22	19	thermometer binary "or"	USPAT	SAME	OFF	2004/11/18 14:57
L18	22	thermometer binary AND adders	USPAT	WITH	OFF	2004/11/18 13:43
L6	25	thermometer binary SAME gate	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2004/11/18 12:46

L20	27	thermometer ADJ1 binary	USPAT	AND	OFF	2004/11/18 14:43
L19	51	(341/96).CCLS.	USPAT	OR	OFF	2004/11/18 13:54
L17	58	thermometer binary AND adder	USPAT	WITH	OFF	2004/11/18 13:25
L16	73	thermometer binary AND adder	USPAT	SAME	OFF	2004/11/18 13:25
L1	220	thermometer code binary	USPAT	WITH	OFF	2004/11/18 12:18
L10	787	((341/160) or (341/102) or (341/103) or (341/159)).CCLS.	USPAT	OR	OFF	2004/11/18 12:58
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1 Fat tree encoder design for ultra-high speed flash A/D converters*Daegy Lee; Jincheol Yoo; Kyusun Choi; Ghaznavi, J.;*

Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Symposium on , Volume: 2 , 4-7 Aug. 2002

Pages:II-87 - II-90 vol.2

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2 A self-trimming 14-b 100-MS/s CMOS DAC*Bugeja, A.R.; Bang-Sup Song;*

Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 12 , Dec. 2000

Pages:1841 - 1852

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3 A novel coding scheme for the ROM of parallel ADCs, featuring reduced conversion noise in the case of single bubbles in the thermometer code*Padoan, S.; Boni, A.; Morandi, C.; Venturi, F.;*

Electronics, Circuits and Systems, 1998 IEEE International Conference on , Volume: 2 , 7-10 Sept. 1998

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4 A 4GS/s 6b flash ADC in 0.13 μ m CMOS*Paulus, C.; Bluthgen, H.-M.; Low, M.; Sicheneder, E.; Bruls, N.; Courtois, A.; Tiebout, M.; Thewes, R.;*

VLSI Circuits, 2004. Digest of Technical Papers. 2004 Symposium on , 17-19 Oct. 2004

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5 The investigation of mercury presence in human blood: an extrapolation from animal data using neural networks

Hashemi, R.R.; Bahar, M.; Tyler, A.A.; Young, J.;

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7 Clock-jitter induced distortion in high speed CMOS switched-current segmented digital-to-analog converters

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Van Den Bosch, A.; Borremans, M.; Steyaert, M.; Sansen, W.;

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9 Efficient FPGA-based multistage two-path decimation filter for noise thermometer

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10 A complete 18-bit audio D/A converter

Baginski, P.; Brokaw, P.; Wurcer, S.;

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13 **A precision CTD microprofiler**

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14 **Flash analog-to-digital converter using resonant-tunneling multiple valued circuits**

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Multiple-Valued Logic, 2001. Proceedings. 31st IEEE International Symposium on , 22-24 May 2001

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15 **A 14 b 150 Msample/s update rate Q^2 random walk CMOS DAC**

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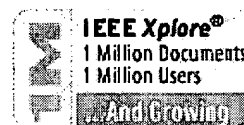
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